









FIG. 5

4/5 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 f(k-8)f(k-7) f(k-6) f{k-5} f{k-4} f(k-3) f{k-2} 7(K-1) ((k) F(k+1) f(k+2) f(k+3) f(k+4) f{k+5} f{k+6} f(k+7)CAMP data ID packet Half-slot boundary Slot boundary FIG. 7 A Α В data B data\_A clock clock 10 24s FIG. 8 FIG. 9 Α Α data\_A data\_B data\_B data\_B 1 clock data\_A clock

